ABSTRUCT OF THE DISCLOSURE

In a conventional LSI, since a minimum chip size is inevitably determined by the number and size of input/output pads formed on a chip, an no-patterned region occurs in an active region surrounded by an I/O region in a highly integrated circuit or a circuit with a small number of gates. The present invention intends to solve this problem to improve a semiconductor device. In a semiconductor device comprising on the same chip at least an I/O region where an input/output pad is formed and active regions where a circuit can be mounted, a plurality of logic circuits having the same functions or different functions are mounted in the active regions on the same chip.